



Attorney Docket No. 03W089

- ☒ Original
- ☐ Continuation
- ☐ Division
- ☐ Continuation-in-part
- ☐ Supplemental

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name.

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled

### Low Power Current Input Delta-Sigma ADC Using Injection FET Reference

the specification of which

(check one) ☐ is attached hereto.

☒ was filed on 01/29/2004 as Application No. 10/769,111 and (a) [other than supplemental] was amended on \_\_\_\_\_ or (b) [supplemental] with amendments through n/a.

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose to the United States Patent and Trademark Office all information known to me to be material to patentability as defined in Title 37, Code of Federal Regulations, §1.56.

I hereby claim foreign priority benefits under Title 35, United States Code, §119 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

Prior Foreign Application(s)

Priority Claimed

☐ Yes ☐ No

Number	Country	Day/Month/Year Filed
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I hereby claim the benefit under Title 35, United States Code, §120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code §112, I acknowledge the duty to disclose to the United States Patent and Trademark Office all information known to me to be material to patentability as defined in Title 37, Code of Federal Regulations, §1.56 which became available between the filing date of the prior application and the national or PCT international filing date of this application:

Application Serial No.	Filing Date	Status
		(patented, pending, abandoned)

Raytheon Company

Intellectual Property & Licensing, Loc: EO/E04/N119

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El Segundo CA 90245-0902, Attorney: W. C. Schubert (805) 562-2685 phone


re: Low Power Current Input Delta-Sigma ADC Using Injection FET Reference

I hereby appoint the following attorneys, or agent and attorneys, to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith:

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I HEREBY DECLARE THAT ALL STATEMENTS MADE HEREIN OF MY OWN KNOWLEDGE ARE TRUE AND THAT ALL STATEMENTS MADE ON INFORMATION AND BELIEF ARE BELIEVED TO BE TRUE; AND FURTHER THAT THESE STATEMENTS WERE MADE WITH THE KNOWLEDGE THAT WILLFUL FALSE STATEMENTS AND THE LIKE SO MADE ARE PUNISHABLE BY FINE OR IMPRISONMENT, OR BOTH, UNDER SECTION 1001 OF TITLE 18 OF THE UNITED STATES CODE AND THAT SUCH WILLFUL FALSE STATEMENTS MAY JEOPARDIZE THE VALIDITY OF THE APPLICATION OR ANY PATENT ISSUED THEREON.

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\*Government contract related N66001-02-C-8078

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